

From the INTERNATIONAL BUREAU **PCT** To: NOTIFICATION OF THE RECORDING **OF A CHANGE** F.B. RICE & CO. 605 Darling Street (PCT Rule 92bis.1 and Balmain, NSW 2041 Administrative Instructions, Section 422) **AUSTRALIE** Date of mailing (day/month/year) 18 September 2000 (18.09.00) Applicant's or agent's file reference IMPORTANT NOTIFICATION 84680 International application No. International filing date (day/month/year) PCT/AU99/00511 24 June 1999 (24.06.99) 1. The following indications appeared on record concerning: the applicant the inventor the agent the common representative State of Nationality State of Residence Name and Address ΑU AU ARISTOCRAT LEISURE INDUSTRIES PTY. Telephone No. 71 Longueville Road Lane Cove, NSW 2066 Australia Facsimile No. Teleprinter No. 2. The International Bureau hereby notifies the applicant that the following change has been recorded concerning: the nationality the residence the person the name the address State of Nationality State of Residence Name and Address ΑU ΑU ARISTOCRAT TECHNOLOGIES AUSTRALIA **PTY LTD** Telephone No. 71 Longueville Road Lane Cove, NSW 2066 Australia Facsimile No. Teleprinter No. 3. Further observations, if necessary: 4. A copy of this notification has been sent to: X the receiving Office the designated Offices concerned the elected Offices concerned the International Searching Authority the International Preliminary Examining Authority other: Authorized officer The International Bureau of WIPO 34, chemin des Colombettes Christine Carrié 1211 Geneva 20, Switzerland

Telephone No.: (41-22) 338.83.38

Facsimile No.: (41-22) 740.14.35





From the INTERNATIONAL BUREAU

PCT

NOTIFICATION OF ELECTION

(PCT Rule 61.2)

To:

Assistant Commissioner for Patents United States Patent and Trademark Office Box PCT Washington, D.C.20231 ÉTATS-UNIS D'AMÉRIQUE

Date of mailing (day/month/year)
16 February 2000 (16.02.00)

International application No.
PCT/AU99/00511

International filing date (day/month/year)
24 June 1999 (24.06.99)

Applicant

CURTIS, Keith, Edwin et al

1.	The designated Office is hereby notified of its election made:
	X in the demand filed with the International Preliminary Examining Authority on:
	24 January 2000 (24.01.00)
	in a notice effecting later election filed with the International Bureau on:
2.	The election X was was not
	made before the expiration of 19 months from the priority date or, where Rule 32 applies, within the time limit under Rule 32.2(b).

The International Bureau of WIPO 34, chemin des Colombettes 1211 Geneva 20, Switzerland Authorized officer

Jean-Marc Vivet

Telephone No.: (41-22) 338.83.38

Facsimile No.: (41-22) 740.14.35

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference 84680		See Notification of Transmittal of International Preliminary Examination Report (Form PCT/IPEA/416).				
International application No.	International filing date (day/month/year) P		Priority Date (day/month/year)			
PCT/AU99/00511	24 June 1999		24 June 1998			
International Patent Classification (IPC	or national classification a	and IPC				
Int. Cl. ⁷ G06F 17/00						
Applicant						
ARISTOCRAT LEISURE	INDUSTRIES PTY LTD					
This international preliminary Authority and is transmitted to			International Preliminary Examining			
2. This REPORT consists of a to	tal of 3 sheets, including	this cover sheet.				
been amended and are the	This report is also accompanied by ANNEXES, i.e., sheets of the description, claims and/or drawings which have been amended and are the basis for this report and/or sheets containing rectifications made before this Authority (see Rule 70.16 and Section 607 of the Administrative Instructions under the PCT).					
These annexes consist of a total	al of sheet(s).		·			
3. This report contains indications relat	ing to the following items:					
I X Basis of the repor	t ·					
II Priority						
III Non-establishmen	nt of opinion with regard to	novelty, inventive	step and industrial applicability			
IV Lack of unity of i	nvention					
l — —	nt under Article 35(2) with anations supporting such st		inventive step or industrial applicability;			
VI Certain document	s cited					
VII Certain defects in	the international application	on				
VIII Certain observation	ons on the international app	olication				
Date of submission of the demand 24 January 2000		Date of completion of the report 23 February 2000				
Name and mailing address of the IPEA/AU		orized Officer				
AUSTRALIAN PATENT OFFICE PO BOX 200, WODEN ACT 2606, AUSTRALIA E-mail address: pct@ipaustralia.gov.au		CATHERINE REES				
Facsimile No. (02) 6285 3929		ohone No. (02) 628	3 2555			



International application No.

PCT/AU99/00511

I.	Basis of the report	
1.	With regard to the elements of the international application:*	
	X the international application as originally filed.	
	the description, pages, as originally filed,	
	pages, filed with the demand,	
	pages , filed with the letter of	
	the claims, pages, as originally filed,	
	pages , as amended (together with any statement) under Article 19,	
	pages , filed with the demand,	
	pages, filed with the letter of.	
	the drawings, pages, as originally filed,	
	pages , filed with the demand,	
	pages, filed with the letter of.	
	the sequence listing part of the description:	
	pages , as originally filed	:
	pages, filed with the demand	
	pages , filed with the letter of .	
2.	With regard to the language, all the elements marked above were available or furnished to this Authority in the language is which the international application was filed, unless otherwise indicated under this item. These elements were available or furnished to this Authority in the following language which is:	n
	the language of a translation furnished for the purposes of international search (under Rule 23.1(b)).	
	the language of publication of the international application (under Rule 48.3(b)).	
	the language of the translation furnished for the purposes of international preliminary examination (under Rules 55.3 and/or 55.3).	2
3.	With regard to any nucleotide and/or amino acid sequence disclosed in the international application, was on the basis of the sequence listing:	
	contained in the international application in written form.	
	filed together with the international application in computer readable form.	
	furnished subsequently to this Authority in written form.	
	furnished subsequently to this Authority in computer readable form.	
	The statement that the subsequently furnished written sequence listing does not go beyond the disclosure in the international application as filed has been furnished.	
	The statement that the information recorded in computer readable form is identical to the written sequence listing habeen furnished	s
4.	The amendments have resulted in the cancellation of:	
	the description, pages	
	the claims, Nos.	
	the drawings, sheets/fig.	
5.	This report has been established as if (some of) the amendments had not been made, since they have been considered to go beyond the disclosure as filed, as indicated in the Supplemental Box (Rule 70.2(c)).**	
*	Replacement sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally filed" and are not annexed to this report since they do not contain amendments (Rules 70.16 and 70.17). Any replacement sheet containing such amendments must be referred to under item 1 and annexed to this report	5



International application No.

PCT/AU99/00511

V.	Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability;
	citations and explanations supporting such statement

1.	Statement		
	Novelty (N)	Claims 1 - 8	YES
	,	Claims	NO
	Inventive step (IS)	Claims 1 - 8	YES
		Claims	NO
	Industrial applicability (IA)	Claims 1 - 8	YES
		Claims	NO

2. Citations and explanations (Rule 70.7)

Citations:

EP 464811

Novelty (N) and Inventive Step (IS):

The above citation does not disclose or even suggest the invention as claimed. The invention must therefore be considered to be both novel and inventive.



co E/m

PCT

NOTIFICATION OF THE RECORDING OF A CHANGE

(PCT Rule 92bis.1 and Administrative Instructions, Section 422)

F.B. RICE & CO. 605 Darling Street Balmain, NSW 2041 AUSTRALIE FROM the INTERNATIONAL BUREAU RECEIVED 2 & SE 2000
605 Darling Street Balmain, NSW 2041 AUSTRALIE IMPORTANT NOTIFICATION

Date of mailing (day/month/year) 18 September 2000 (18.09.00)	CO /					
Applicant's or agent's file reference 84680	IMPORTANT NOTIFICATION					
International application No.	International filing date (day/month/year)					
PCT/AU99/00511	24 June 1999 (24.06.99)					
The following indications appeared on record concerning:						
X the applicant the inventor	the agent the common representative					
Name and Address	State of Nationality State of Residence					
ARISTOCRAT LEISURE INDUSTRIES PTY. LTD.	AU AU					
71 Longueville Road Lane Cove, NSW 2066	Telephone No.					
Australia	Facsimile No.					
	Teleprinter No.					
2. The International Bureau hereby notifies the applicant that the	ne following change has been recorded concerning:					
the person X the name the addr	ress the nationality the residence					
Name and Address	State of Nationality State of Residence					
ARISTOCRAT TECHNOLOGIES AUSTRALIA PTY LTD	AU AU					
71 Longueville Road	Telephone No.					
Lane Cove, NSW 2066 Australia	Facsimile No.					
	r dosinine 140.					
	Teleprinter No.					
3. Further observations, if necessary:						
4. A copy of this notification has been sent to:						
X the receiving Office	the designated Offices concerned					
the International Searching Authority	X the elected Offices concerned					
the International Preliminary Examining Authority	other:					

The International Bureau of WIPO 34, chemin des Colombettes 1211 Geneva 20, Switzerland Authorized officer

L. LAREIE Christine Carrié

Facsimile No.: (41-22) 740.14.35 Telephone No.: (41-22) 338.83.38

Form PCT/IB/306 (March 1994)

003530088



REQUEST

The undersigned requests that the present international application be processed according to the Patent Cooperation Treaty

	rec. ing Office use only							
International Application No.								
International Filin	g Date							
	Office and "PCT International Application"							
Applicant's or ager	nt's file reference aracters maximum) 84680							
TULATOR APPAR	ATUS .							
tity, full official he country of the idence if no State	This person is also inventor.							
	Telephone No							
	Facsimile No.							
	Teleprinter No.							
	د .							
State (that is, count	try) of residence: AU							
	the United States the States indicated in the Supplemental Box							
IER) INVENTOR(S)								

Box No I TITLE OF INVENTION				
VIRTUAL EPROM SIM	ULATOR APPARATUS			
Box No. II APPLICANT				
Name and address: (Family name followed by given name; for a legal ent designation. The address must include postal code and name of country. The address indicated in this Box is the applicant's State (that is, country) of resi of residence is indicated below.)	lence if no State This person is a	lso inventor.		
Aristocrat Leisure Industries Pty Ltd	Telephone No			
71 Longueville Road				
Lane Cove 2066	Facsimile No.	,		
New South Wales				
Australia	Teleprinter No.	Teleprinter No.		
		د .		
State (that is, country) of nationality: AU	State (that is, country) of residence:			
This person is applicant all designated all designated for the purposes of: all designated the United States of	ts except the United States f America of America only	the States indicated in the Supplemental Box		
Box No. III FURTHER APPLICANT(S) AND/OR (FURTH	ER) INVENTOR(S)			
Name and address: (Family name followed by given name: for a legal enti- designation. The address must include postal code and name of country. The address indicated in this Box is the applicant's State (that is, country) of residence is indicated below.)	v. full official country of the ence if no State This person is:			
Curtis, Keith Edwin	applicant only			
138 Park Ridge Lane				
Henderson	applicant and it	nventor		
Nevada 89015				
USA		If this check-box not fill in below.)		
State (that is, country) of nationality: US	State (that is, country) of residence: US	. •		
This person is applicant all designated all designated State for the purposes of: States all designated the United States or		the States indicated in the Supplemental Box		
Further applicants and/or (further) inventors are indicated or				
Box No. IV AGENT OR COMMON REPRESENTATIVE;	OR ADDRESS FOR CORRESPONDENC	E		
The person identified below is hereby/has been appointed to act on the applicant(s) before the competent International Authorities as:	ehaif of agent com	unon representative		
Name and address: (Family name followed by given name: for a legal entity designation. The address must include postal code and	full official Telephone No			
	(612) 9810 7133			
F B RICE & CO	Facsimile No.			
605 Darling Street	((12) 2812 8222			
BALMAIN NSW 2041	(612) 9810 8200			
AUSTRALIA	Teleprinter No.			
Address for correspondence: Mark this check-box where ac	agent or common representative is/has been	appointed and the		

space above is used instead to indicate a special address to which correspondence should be sent.

	eet No 2	
Continuation of Box No. III HER APPLICANTS AI	ND/OR (FURTHER	ENTORS
If none of the following sub-boxes is used,	this sheet is not to b	e included in the request
Name and address: (Family name followed by given name; for a legal en designation. The address must include postal code and name of country. T address indicated in this Box is the applicant's State (that is, country) of residence is indicated below.)	he country of the	This person is:
Bond, Eugene Thomas 6329 Lena King Avenue	•	applicant only
Las Vegas 89120 Nevada		applicant and inventor
USA		inventor only (If this check-box
State (that is, country) of nationality: US	State (that is, com	is marked, do not fill in below.) ntry) of residence: US
This person is applicant all designated all designated States all designated States all designated States		the United States the States indicated in of America only the Supplemental Box
Name and address: (Family name followed by given name: for a legal endesignation. The address must include postal code and name of country. The address indicated in this Box is the applicant's State (that is, country) of rest of residence is indicated below.)	ia country of the	This person is:
		applicant only
		applicant and inventor
		inventor only (If this check-box is marked, do not fill in below.)
State (that is, country) of nationality:	State (that is, coun	
This person is applicant all designated all designated States all designated States the United States		the United States the States indicated in the Supplemental Box
Name and address: (Family name followed by given name: for a legal enti- designation. The address must include postal code and name of country. The address indicated in this Box is the applicant's State (that is, country) of resi- of residence is indicated below.)	ity, jull official e country of the dence if no State	This person is:
		applicant only
		applicant and inventor
•		inventor only (If this check-box is marked, do not fill in below.)
State (that is, country) of nationality:	State (that is, count	try) of residence:
This person is applicant all designated all designated State the United States	of America	the United States the States indicated in the Supplemental Box
Name and address: (Family name followed by given name; for a legal enti- designation. The address must include postal code and name of country. The address indicated in this Box is the applicant's State (that is, country) of resion of residence is indicated below.)	ty, full official e country of the lence if no State	This person is:
		applicant only
		applicant and inventor
		inventor only (If this check-box is marked, do not fill in below.)
State (that is, country) of nationality:	State (that is, count	
This person is applicant all designated all designated States the United States of		the United States the States indicated in the Supplemental Box
Further applicants and/or (further) inventors are indicated on Form PCT/RO/101 (continuation sheet) (January 1997)	another continuation	
Otto to r (communication sheet) (landary 1997)		See Notes to the request form

	x No.							
Th	e tollo	owing designations are hereby made under Rule 4.9(a) (n	ıark ı	he appli	icable check-boxes; at least one must be marked:			
Re	gional	l Patent						
	AP	ARIPO Patent: GH Ghana, GM Gambia, KE Kenya, ZW Zimbabwe, and any other State which is a Contract	LS I	esotho, State of t	MW Malawi, SD Sudan, SZ Swaziland, UG Uganda he Harare Protocol and of the PCT			
	EA	Eurasian Patent: AM Armenia, AZ Azerbaijan, BY Belarus, KG Kyrgyzstan, KZ Kazakstan, MD Republic o Moldova, RU Russian Federation, TJ Tajikistan, TM Turkmenistan, and any other State which is a Contracting State of the Eurasian Patent Convention and of the PCT						
	EP	European Patent: AT Austria, BE Belgium, CH and LI Switzerland and Liechtenstein, CY Cyprus, DE Germany DK Denmark, ES Spain, FI Finland, FR France, GB United Kingdom, GR Greece, IE Ireland, IT Italy, LU Luxembourg MC Monaco, NL Netherlands, PT Portugal, SE Sweden, and any other State which is a Contracting State of the European Patent Convention and of the PCT						
		OAPI Patent: BF Burkina Faso, BJ Benin, CF Centre GA Gabon, GN Guinea, GW Guinea-Bissau, ML Mali any other State which is a member State of OAPI and a desired specific or dested lively.	, MIR a Cor	Maurita tracting	ania, NE Niger, SN Senegal, TD Chad, TG Togo, and State of the PCT (if other kind of protection or treatmen			
Na	tianal	desired, specify on dotted line)						
l Na		Patent (if other kind of protection or treatment desired, specify	on de	^				
님	AE	United Arab Emirates	느	LS	Lesotho			
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	AU	Australia	느	MD	Republic of Moldova			
님	ΑZ	Azerbaijan	느	MG	Madagascar			
	BA	Bosnia and Herzegovina		MK	The former Yugoslav Republic of Macedonia			
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	KR	Democratic People's Republic of Korea		ZW	Zimbabwe			
	KZ	Republic of Korea			s reserved for designating States (for the purposes of patent) which have become party to the PCT after			
=		Kazakstan			this sheet:			
_	LC	Saint Lucia	\Box					
_	LK	Sri Lanka	Ħ					
	LR	Liberia						

Precautionary Designation Statement: In addition to the designations made above, the applicant also makes under Rule 4.9(b) all other designations which would be permitted under the PCT except any designation(s) indicated in the Supplemental Box as being excluded from the scope of this statement. The applicant declares that those additional designations are subject to confirmation and that any designation which is not confirmed before the expiration of 15 months from the priority date is to be regarded as withdrawn by the applicant at the expiration of that time limit. (Confirmation of a designation consists of the filing of a notice specifying that designation and the payment of the designation and confirmation fees. Confirmation must reach the receiving Office within the 15-month time limit.)

Box No VI	PRIORITY	CLAI		Sheet N		indicated in the Supplemental	l Box
Filin	g date		Jumber		Where earlier ap		
of earlier	application onth/year		er application	national application:	regional application:		
item (1) 24 Ju		60	/090514	US			
item (2)	5.98)						 -
item (3)							
of the earlier a purposes of th *Where the earl Convention for t	application(s) (of e present intermiter application is the Protection of I	only if the national ap an ARIPO d Industrial P	earlier applica pplication is th application, it is i roperty for which	ition was filed with e receiving Office) mandatory to indicato n that earlier applicat	Irnational Bureau a certifice the Office which for the identified above as item(in the Supplemental Box at light was filed (Rule 4.10(b)(ii)	s): least one country party to the Par	ris .
Box No VII				AUTHORITY			
(if two or more I competent to car	national Searchin nternational Sear ry out the interna osen; the two-leti	ching Autho tional searc	orities are ch, indicate	Request to use rearlier search has lead the Authority): Date (day/month)	een carried out by or reques	reference to that search (if the search of t	ning
Box No VIII	CHECK LIS	T; LANO	GUAGE OF F	ILING			
request description (ex sequence listin claims abstract drawings: sequence listin of description Total number Figure of the c should accomp Box No IX Next to each signatu	g part): g part of sheets: Irawings which any the abstract	11 2 1 5 5 23 DF APPL	1. fee cal 2. separa 3. copy o 4. statem 5. priority 6. ransla 7. separat 8. nucleof 9. other (s	culation sheet te signed power of f general power of ent explaining lack v document(s) ider tion of internations te indications conc tide and/or amino a specify); Language of filin international appli GENT	attorney: reference number of signature tified in Box No. VI as ited application into (langual erning deposited microorgated sequence listing in congressions of the cation: English	er, if any: em(s): 60/090514 age): ganism or other biological mat	terial
				For receiving Office	e use only		
	tual receipt of t nal application:	he purpor	ted			2. Drawings:	
 Corrected timely rectangle the purpor 	date of actual re eived papers or ted internationa	drawings il applicat	completing ion:			received	
corrections	nely receipt of t s under PCT Ar nal Searching A	ticle 1 (2)		6. Transn	nittal of search copy delay	not received	i
(if two or i	more are compe	tent):	ISA/		fee is paid	oo amm	
	ceipt of the reco		For Inter	national Bureau us	e only		

The Commissioner of Patents PO Box 200

WODEN ACT 2606

F B RICE & CO SYDNEY NSW Speed Dial 511

24 June 1999

Our Ref: 84680 Contact: Chris Owens

PATENTS

Dear Sir

Statement Explaining Lack of Signature Aristocrat Leisure Industries Pty Ltd, Keith Edwin Curtis, Eugene Thomas Bond International Patent Application Entitled: "Virtual EPROM simulator apparatus"

With respect to the above new application, we advise that we cannot file a Power of Attorney on behalf of the applicant/inventors Keith Edwin Curtis and Eugene Thomas Bond.

Messrs Curtis and Bond were the applicants and inventors named in the priority application which was filed in USA on 24 June 1998 as US Provisional Application Serial No. 60/90514.

Although our US associates have attempted to make contact, Messrs Curtis and Bond have been unavailable for signature since Aristocrat Leisure Industries Pty Ltd acquired the rights in the invention.

Yours respectfully F B RICE & CO

CO/ih/e18/fbg0002

IPEA/

PCT

CHAPTER II

DEMAND

under Article 31 of the Patent Cooperation Treaty:

The undersigned requests that the international application specified below be the subject of international preliminary examination according to the Patent Cooperation Treaty and hereby elects all eligible States (except where otherwise indicated).

For !	International Preliminary	y Examining Authority	use only	
				
Identification of IPEA		Date of receipt of DE	EMAND	
Box No. I IDENTIFICATION OF T	THE INTERNATIONA	L APPLICATION	Applicant's or agent's file reference 84680	
International application No.	International filing date		(Earliest) Priority date (day/month/year)	
PCT/AU99/00511	24 June 199	99 (24.6.99)	24 June 1998 (24.6.98)	
Title of invention	<u> </u>			
Virtual EPROM simulator app	paratus			
Box No. II APPLICANT(S)	-			
Name and address: (Family name followed by giv. The address must include pos	en name; for a legal entity, f tal code and name of country	full official designation.	Telephone No	
ARISTOCRAT LEISU 71 Longueville Road Lane Cove 2066	JRE INDUSTRIES PT	TY LTD	Facsimile No.	
New South Wales			Teleprinter No.	
State (that is, country) of nationality:		State (that is, country	y) of residence:	
AU	İ		AU	
Name and address: (Family name followed by given	name; for a legal entity, full of	ficial designation. The addre		
CURTIS, Keith Edwir 138 Park Ridge Lane Henderson NV 89015 USA	n			
State (that is, country) of nationality:		State (that is, country,) of residence:	
US			US	
Name and address: (Family name followed by given in BOND, Eugene Thoma 6329 Lena King Avenu Las Vegas NV 89120 USA	as	Ficial designation. The addre	ss must include postal code and name of country.)	
State (that is, country) of nationality:		State (that is, country)) of residence:	
US		US		
Further applicants are indicated on a c	continuation sheet.	-	00	

Sheet No. 2

international application No.

PCT/AU99/00511

Box No. III AGENT OR COMMON REPRESENTATIVE; OR ADDRESS FOR	CORRESPONDENCE
The following person is agent common representative	
and has been appointed earlier and represents the applicant(s) also for international p	oreliminary examination.
is hereby appointed and any earlier appointment of (an) agent(s)/common represe	entative is hereby revoked.
is hereby appointed, specifically for the procedure before the International Prelin addition to the agent(s)/common representative appointed earlier.	ninary Examining Authority, in
Name and address: (Family name followed by given name; for a legal entity, full official designation. The address must include postal code and name of country.)	Telephone No
F B RICE & CO	(612) 9810 7133
605 Darling Street	Facsimile No.
BALMAIN NSW 2041	(612) 9810 8200
AUSTRALIA	(612) 9810 8200 Teleprinter No.
	Totopinio No.
Address for correspondence: Mark this check-box where no agent or common represented above is used instead to indicate a special address to which correspondence shall be a special address.	resentative is/has been appointed and the nould be sent.
Box No. IV BASIS FOR INTERNATIONAL PRELIMINARY EXAMINAT	TION
Statement concerning amendments:* 1. The applicant wishes the international preliminary examination to start on the basis of the international application as originally filed the description as originally filed as amended under Article 34 the claims as originally filed as amended under Article 19 (together with any accompanying stars amended under Article 34 the drawings as originally filed as amended under Article 34 2. The applicant wishes any amendment to the claims under Article 19 to be considered The applicant wishes the start of the international preliminary examination to be postpored from the priority date unless the International Preliminary Examining Authority receives a Article 19 or a notice from the applicant that he does not wish to make such amendments (International preliminary with the such amendments (International preliminary with a preli	atement) d as reversed. ned until the expiration of 20 months
* Where no check-box is marked, international preliminary examination will start on the basis of as originally filed or, where a copy of amendments to the claims under Article 19 and/or amendments and Article 34 are received by the International Preliminary Examining Authority before it has no the international preliminary examination report, as so amended.	dments of the international application
Language for the purposes of international preliminary examination: . ENGLIS which is the language in which the international application was filed. which is the language of a translation furnished for the purposes of international se which is the language of publication of the international application. which is the language of the translation (to be) furnished for the purposes of international se which is the language of the translation (to be) furnished for the purposes of international se which is the language of the translation (to be) furnished for the purposes of international se which is the language of the translation (to be) furnished for the purposes of international se which is the language of the translation (to be) furnished for the purposes of international se which is the language of publication (to be) furnished for the purposes of international se which is the language of publication (to be) furnished for the purposes of international section. Box No. V ELECTION OF STATES The applicant hereby elects all eligible States (that is, all States which have been designated as the PCT) excluding the following States which the applicant wishes not to elect:	arch. ational preliminary examination.
	1

Sheet No. 3

international application No.

PCT/AU99/00511

Box No. VI CHECKLIST					
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INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶:

G06F 17/00

(11) International Publication Number: WO 99/67721

(43) International Publication Date:

(43) International Publication Date: 29 December 1999 (29.12.99)

(22) International Filing Date:

(21) International Application Number:

24 June 1999 (24.06.99)

PCT/AU99/00511

(30) Priority Data:

60/090,514

24 June 1998 (24.06.98)

US

(81) Designated States: AU, JP, NZ, US, ZA, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).

Published

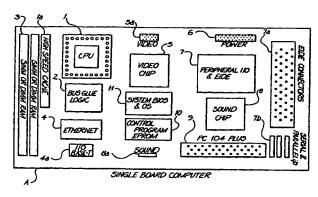
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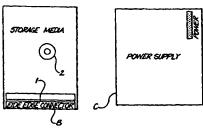
- (71) Applicant (for all designated States except US): ARISTO-CRAT LEISURE INDUSTRIES PTY. LTD. [AU/AU]; 71 Longueville Road, Lane Cove, NSW 2066 (AU).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): CURTIS, Keith, Edwin [US/US]; 138 Park Ridge Lane, Henderson, NV 89015 (US). BOND, Eugene, Thomas [US/US]; 6329 Lena King Avenue, Las Vegas, NV 89120 (US).
- (74) Agent: F.B. RICE & CO.; 605 Darling Street, Balmain, NSW 2041 (AU).

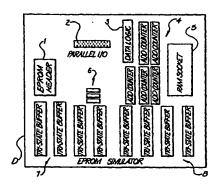
(54) Title: VIRTUAL EPROM SIMULATOR APPARATUS

(57) Abstract

An apparatus for simulating the internal configuration of industry standard ROM and EPROM-type chips using other types of storage technologies, while still operating transparently with interfaces and mechanisms such as authentication devices adapted to EPROM-type The invention includes: media. an EPROM connector interface, a data presentation program; user access log display program; a user login/registration program; a software/data library; software/data selection program; and software/data loader program. These components work in conjunction to securely retrieve software images resident in mass storage media and to present them to an authentication device as if the images were resident in EPROM type media. The invention is particularly adapted to use in the gaming industry where regulation and fraud detection are performed using **EPROM** authentication techniques.







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VIRTUAL EPROM SIMULATOR APPARATUS

Introduction

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This invention relates to the technology of non-volatile memory components commonly known as read-only memory ("ROM"), programmable ROM ("PROM"), and erasable PROM ("EPROM") integrated circuit ("IC") chips. Hereafter, the term "EPROM" will be used to refer to this category of ICs collectively. The present invention is concerned in particular with how to utilize other storage media with various interfaces adapted to the current configuration of standard, commercially available EPROMs.

EPROMs provide circuit designers and computer-based product manufacturers a compact, convenient means of storing programs and data on a single board and, often, single chip. Using EPROMs, products executing relatively static and compact stored programs may be engineered without resort to bulkier storage technologies such as fixed drives, CD-ROMs or diskettes. On the other hand, devices specifically designed for EPROM retrieval are not readily adaptable to retrieve code and data from other storage technologies, when additional capacity and/or newer storage technologies may become desirable.

While highly reliable with minimal mean time between failures, a known disadvantage of EPROMs is that they incur more intermittent and permanent retrieval errors than other kinds of memories such as RAM ICs. For example, the stored contents of an EPROM may degrade over time resulting in state changes to one or more bits. Apparent or intermittent errors may result from synchronization faults occasioned by the generally slower access times of EPROMs compared with faster random access memory (RAM)-type memories. Accordingly, one important category of EPROM interfaces consists of testing devices which rely on the present internal configuration of EPROM chips in order to analyze and verify the integrity of their stored data. Numerous other devices, boards and types of interfaces depend on EPROMs as these are presently configured. The present invention utilizes other storage media while emulating EPROMs for purposes of such devices.

Background of the Invention

The present invention is particularly adapted to the field of casino gaming devices which house one or more games on internal EPROMs. Such devices include what are commonly known as slot machines, video poker

WO 99/67721 2

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machines, and more recent implementations that have combined both and may offer a variety of games on a single machine, referred to as a "multigame." EPROMs employed in gaming applications are naturally subject to the inherent limitations and potential faults of EPROMs in any other computer-based apparatus. However, the gaming industry fosters other reasons for assuring the integrity of EPROM contents: regulatory compliance and, due to the possibility of instant pecuniary gain or loss, detecting and thwarting cheaters who could benefit from purposely altered software or payout tables.

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In jurisdictions where gaming is permitted, use of such devices is regulated by law. Consequently, in regulated jurisdictions gaming devices are routinely, regularly and randomly verified by the authorities to ensure the compliance, integrity and authenticity of their EPROMs. The present invention is particularly designed to aid in complying with gaming regulations requiring an EPROM-based method for determining the authenticity of the device. A commonly configured multi-game machine would include a number of games such as: keno, poker, slots, blackjack and others. These games can be played separately or be combined into new games, games within games, pushing the limits of software and hardware. Typical gaming machines of this type also employ a combination of mechanical devices, electronics, microprocessors and complex software to generate the gaming experience. Some of the common hardware components used are as follows: a cabinet, handle, jackpot tower, coin acceptor, bill acceptor, credit meters, back-lit glass, reels, monitor, game door, buttons, payout hopper, lights and speakers. The electronics include many of the following components: microprocessor, read only memory (ROM), RAM, high speed data bus, peripheral logic chips for serial and parallel ports, driver circuitry for lamps, speakers, video and other devices. Typical software components would include: power-up initialization, device drivers, game recovery, state machine, random number generator, payout routine, credit management, graphics engine, sound, game engine, game data, security, accounting and reporting functions. As a result, the increasing complexity of such gaming devices commensurately increases the difficulty of ensuring EPROM integrity as well as regulatory compliance.

Software authentication is usually a process carried out by a third party (other than the manufacturer or the casino operator) representing the

gaming enforcement agency that has jurisdiction over the device. The purpose is to ensure that the software controlling the game has not been tampered with, and software authentication is usually required after a large jackpot has been obtained by a player. Authentication also verifies that the gaming software was previously examined and approved by the gaming agency in whose jurisdiction the jackpot occurred. Moreover, casinos wish to verify that the software running the game is legitimate, particularly if a particular machine is not earning the expected amount of revenue, or in response to player complaints about the behavior of a particular game.

Disadvantages of Prior Art Solutions

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In prior art devices, authentication is usually accomplished by one of two methods. Both methods require opening of the game, removal of the CPU and removal of EPROMs containing the software. In the first method, the removed EPROMs are compared, using an industry standard EPROM programmer/reader, with a custodial (or master) set of EPROMs which have been kept in a secure location. If the comparison indicates that they are the same, the software is considered to be authentic. The second method involves plugging each EPROM into an electronic authentication device, for example a widely used proprietary device marketed under the trade name Kobitron, which generates an authentication identification (ID) for the image resident in the EPROM. The resulting authentication IDS are compared to previously recorded IDs for those EPROMs. If they are identical, the software is verified as authentic.

These methods are common to the gaming industry, but they only apply to software stored in EPROM-style media. U.S. Patent 5,643,086 (Alcorn et al.), assigned to a corporate manufacturer of multi-game devices, discloses an apparatus for authenticating software stored using media other than EPROMs, such as a hard drive. However, in the Alcorn apparatus, the software inspects itself and renders a verdict as to its own authenticity. Self-verification and the potential for compromised security at the point of manufacture render the Alcorn technique a generally unacceptable practice in the gaming industry. In addition, Alcorn's apparatus cannot be used with a Kobitron, upon which regulators and owners have come to rely.

Bond *et al.*, the present inventors, disclosed in a patent application entitled "Software Authentication Control System," US Serial No. 60/089654, filed 17 June 1998, a method for authenticating software stored in non-

EPROM-type media that is consistent with accepted practice. A limitation of this disclosure, however, is that it is also not backwards compatible (at the hardware level) with present EPROM-type authentication devices.

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Present authentication methods are well-suited to prior art devices which use EPROM type storage; however, new operating systems, multi-game devices, and multimedia functionality require new mass storage technologies. Storing gaming software on these technologies render present, accepted, EPROM media authentication devices and procedures useless. And though Alcorn discloses a method for 'authenticating' software stored in mass media, it ignores the existing authentication paradigm presently expected in the gaming industry. Thus, there is a need for a means of authenticating software stored in modern media that is compatible with existing gaming regulations and practices. The industry is comfortable with having a set of EPROMS for "system" software and a set for each model (comprising a unique pay schedule, symbols/graphics, and/or play rules), or a set for each game in a multi-game environment.

In brief, the industry requires a method of authentication which is in harmony with current accepted practices, compatible with current authentication hardware, but which can also avail itself of the advantages of new and future storage technologies.

Definitions

V-PROM

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Virtual programmable read-only memory. In the present invention, V-PROMs are logical containers (like file folders or directories) that contain related stored binary information (software and data). V-PROMs aid in the retrieval and management of related stored binary information by logically grouping discrete data and program entities together as if they were stored in EPROM type media.

V-PROM Registry

A program/data directory which provides crucial information used in the management, retrieval and authentication of related programs and data sets. It is comprised of a relational database directory that was designed to store logical EPROM-like grouping information in V-PROM Registries containing the location and media type of related stored programs and data sets, each group having a unique V-PROM name. of the software games to be installed for a specific application; e.g.,

operator/casino installation.

Summary of the Invention

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The present invention offers a number of advantages over prior art devices, including: backwards compatibility with existing EPROM media authentication devices; the ability to present software and data, such as gaming software, stored in non-ROM media as if it were stored in an EPROM media; added security for protecting the software/data presentation process; functionality to browse a list of all software resident on the machine as if it were in an EPROM library; functionality for selecting resident gaming software for presentation to an authentication device; functionality for registering presentation users; functionality for reporting presentation users and activities.

The present invention, from an apparatus perspective, is a control system for electronic gaming machines which is controlled with a typical microprocessor configuration, including a CPU, RAM and non-volatile storage devices such as a hard drive and/or CD-ROM, input/output interfaces, video display, operating system, power supply, and a plurality of programs and associated tables and other reference data in ROM/EPROM-type media. The ROM/EPROM includes a presentation capability for passing the program and the data to an external device, such as an authenticator, in response to an authorized user request directive. The presentation function includes a user registration program, a V-PROM library which groups related gaming software and data into the logical containers termed "V-PROMs," a V-PROM selection program, a loader program, a data presentation program. A mass storage medium, in communication with the invention control program, containing approved gaming software and related data means associated through V-PROMs is used for non-volatile storage.

The means for emulating an EPROM includes: a pseudo-EPROM memory utilizing some other storage medium, a connection between the EPROM simulator to and from the CPU, a means for addressing the pseudo EPROM memory, an input data buffer, an output data buffer, and an interface for connecting the EPROM emulation means to an industry standard EPROM reader.

From a process perspective, the V-PROM presentation function of the machine's diagnostic routines, which presents mass media based gaming software to ROM media authentication devices, is activated by a key switch

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operation initiated by the user (typically a gaming enforcement agent). Upon entering the V-PROM presentation screen, a log of previous presentation screen users is displayed including: usernames, date, time, type of event, V-PROMs presented/authenticated, and the date and time when then log was created. After display of the log, the user is required to register or exit the diagnostic function. To register, the user must enter a username and password. If the password does not match the previously established password for that account, a user registration error is entered into the log and the screen is automatically exited. New users, if allowed, establish their password during their first login session. Optionally, users can be preenrolled. If the password does match, the collected registration information is appended to the presentation users' log. After registration, the V-PROM selection screen is activated. The user may browse the entire library of V-PROMs resident on the storage media of the machine, termed V-PROM Registry Set. This information includes V-PROM names, description of related gaming software and data, version numbers, creation times and dates, statistical and marketing information. From this screen, by pressing a button, the user can "present" the currently selected V-PROM image (gaming software and related data, logically grouped) to the E-PROM emulator which then can be authenticated by a connected prior art device that is capable of reading EPROMs. After presentation, the user is required to log the results of the authenticating device's inspection. After this, the user can select another V-PROM image for presentation. When the user is finished testing V-PROMs, the screen is exited via a button press, the diagnostic function is ended and normal game play may resume. In the event of a V-PROM failing inspection, upon exiting the diagnostic function, the game enters a lock-up state which can only be cleared by a special process.

V-PROMs can also contain other V-PROMs, thus, a given software configuration can be stored as a V-PROM containing a series of 'smaller' V-PROMs which contain all of the software to be installed for a specific installation, such as a specific casino.

The V-PROM Registry Set has the added benefit of abstracting the type of storage media from the authentication process thus allowing for the use of a wider variety of storage media. For more detail see patent application of Curtis *et al.* for a Software Verification and Authentication Control System.

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Objects of the Invention

It is an object of the present invention to provide a device for use within a microprocessor-based device, which allows for authentication of internal data and software code stored in modern media in manner consistent with that which occurs in current EPROM-based technology.

It is another object of the present invention to provide a device for use within a gaming machine, such as a slot machine or a multi-game machine, which allows for authentication of gaming software stored in modern media in a manner consistent with that of EPROM-based prior art systems.

Still another object of the present invention is to provide a device for use within a gaming machine that allows for gaming software to be authenticated with pre-exiting means through emulating the physical characteristics of gaming software stored in EPROM-type media.

Still another object of the present invention is to provide a method for accessing/presenting gaming software stored in non-EPROM media as if it were stored in EPROM-type media.

Still another object of the present invention is to provide a method of displaying a resident library of gaming software stored in non-ROM media as if it were stored in EPROM-type media.

Still another object of the present invention is to provide a method for securing the gaming software presentation process.

Still another object of the present invention is to provide a method for registering presentation users.

Still another object of the present invention is to provide a method for reporting presentation users and activities.

Still another object of the present invention is to provide a method for selecting gaming software for presentation to an authentication device.

Brief Description of the Drawings

The present invention is more readily understandable by reference to the following detailed description read *in pari materia* with the accompanying drawings in which:

Figure 1 is a simplified schematic diagram of the device of the present invention and related hardware components;

Figure 2 is a block diagram of the EPROM Simulator component detailing process control flow.

Figure 3 is a timing diagram describing the protocol for downloading

to the EPROM simulator.

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Figure 4 is a block diagram describing the V-PROM presentation process.

Detailed Description of the Preferred Embodiment

Figure 1 illustrates the hardware components utilized in the preferred embodiment of this invention. Other hardware configurations are possible because of the modular nature of this design. The present invention is a control system, Figure 1, for a gaming machine. The machine is not shown, but can be either a single game or multi-game machine.

The control system, figure 1, is comprised of four major modules. Module (A) is a drawing of a commercially available Single Board Computer (SBC). Module (B) is a drawing of a commercially available storage media. Module (C) represents the existence of a power supply. Module (D) represents the EPROM Simulator Board. All four components interconnect. Module (A) connects to Module (B) through the use of either an EIDE cable or SCSII cable in the case of a SCSII based SBC. Module (A) also connects to Modules (D) through a parallel I/O cable. Module (C) supplies power to the other four modules via power cables and connectors.

In detail, Module (A), the SBC, is comprised of the following components: (1) an X-86 CPU such as a Pentium $^{\text{TM}}$ or Pentium $^{\text{TM}}$ processor with MMX TM [Pentium, Pentium II and MMX are trademarks of Intel Corp.] or equivalent technology; (1a) high speed processor cache for improved performance; (2) glue logic chips for driving high speed data busses and interfacing the processor to other high speed components such as RAM, video processors, network chips, and I/O boards; (3) high speed RAM sockets, typically DIMM or SIMM style. (4) integrated high speed network interface; (4a) network interface connector; (5) integrated high speed video processor, (5a) video monitor connector; (6) power connector; (7) custom peripheral I/O chip for driving serial I/O, parallel I/O; disk subsets such as EIDE or SCSII; (7a) EIDE or SCSII storage media connectors; (7b) serial and parallel I/O connectors; (8) integrated sound chip; (8a) sound connector; and (9) stackable PC104 Plus connector. It is important to note that this invention, due to hardware abstraction accomplished through the use of a general purpose OS, is not tied to a specific SBC or manufacturer. This invention treats the SBC as a component which can be swapped or upgraded as new boards become commercially available.

Module (B) represents the use of general purpose mass storage media. The media represented in Module (B) can include: Hard disks, CD-ROMS, solid state storage devices, or other common media used in the PC industry. This media is connected (1) to the SBC (A) through a PC industry standard interface such as EIDE, SCSII, or PCMCIA. (2) represents a drive spindle in the case where spinning media is used. Although this invention can function without mass storage, it is specifically designed to exploit the advantages of such devices.

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Module (C) represents a power supply. For the most part, this supply is designed to provide the voltages and current required for using standard PC hardware.

Module (D) represents the EPROM simulator Board. This board is interfaced to the SBC via a parallel cable (not depicted) connect to (A-7b) and (D-2) parallel I/O connectors. This board is responsible for receiving downloaded V-PROM images, stored in RAM (5), allowing for their interrogation by an EPROM reader or Authentication Device attached to EPROM header (1). This board contains the following components: (3) a gate logic chip; (4) five digital counter chips used for addressing RAM (5); (6) three resisters used for biasing the parallel control lines – strobe, autofeed, and init (see fig. 2); (7) four input buffer chips for RAM (5); (8) output buffer chips for RAM (5)

Figure 2 provides more detail on the control system for the EPROM Simulator module (D). Figure 2 describes the following sub-component interactions: control logic (2), components (D-3, 4) responds to parallel I/O control lines, strobe (1b), autofeed (1c) and init (1d), inputs; the strobe input (1b) is used to trigger the control logic to write the data (1a), supplied via the parallel I/O data lines, through the input RAM buffers (3) into the RAM chip (4); the autofeed input (1c) is used to trigger the control logic to increment the address counter; the init input (1d) is used to control access to the RAM buffers (3) either via PC or EPROM control address counter are not selected and held resent for start of next data load from PC. Following data loading and reassertion of the INIT (id) line, an EPROM reader, or Authentication Device, can read the data stored in RAM (4) through the output RAM buffers (5) as if it were reading an EPROM.

The software which controls the functioning of the present invention is stored in three different places. EPROM (Fig 1. A-10) contains the

diagnostic program for the gaming machine. Storage device (A-11) contains the BIOS and Operating System software. The media type of device (A-11) varies from SBC to SBC, in some cases the BIOS and Operating System software is stored in two separate devices. Distinct software games, and data are stored on media device (B). In some configurations, game software can be stored on a fileserver attached through the network via connector (4a).

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The contents of EPROM (Fig. 1, A-10) contains the following software components illustrated in figure 4: diagnostic routines; V-PROM Presentation program (9); user access log presentation program (2); user login/registration program (3); V-PROM Library and selection program (5); and software data loader program (7). V-PROMS are stored in non-volatile storage (B). The V-PROM Registry is stored in secure non-volatile storage not depicted.

Figure 4 details the flow of control during the V-PROM presentation process and is described in the following paragraph. The machines diagnostic routines are triggered by a user via a key switch. Diagnostic routines are selected according to the number of consecutive key switches initiated by the user. Among these diagnostic routines is the V-PROM Presentation Program (1), Figure 4. Upon entering the V-PROM Presentation screen, a log of previous presentation screen users is displayed (2) including: usernames, date, time, type of event, V-PROMS presented/authenticated, and the date and time when then log was created. After display of the log, the user is required to register or exit the diagnostic function (2). To register, the user must enter a username and password (3). If the password does not match (4) the previously established password for that account, a user registration error is entered into the log (4a) and the screen is automatically exited (4b). New users, if allowed, establish their password during their first login session, not depicted. Optionally, users can be pre-enrolled, not depicted. If the password does match, the collected registration information is appended to the presentation users' log, not depicted. After registration, the V-PROM selection screen is activated (5). The user may browse the entire V-PROM registry (7a) library of V-PROMS resident on the storage media (7b) of the machine. This information includes V-PROM names, description of related gaming software and data, version numbers, creation times and dates, statistical and marketing information. From this screen (5), by pressing a button (6), the user can download the selected image to the EPROM emulator (9). To do this, the V-PROM browsing program (5) invokes

the loader program (6) passing it the name of the selected V-PROM. The loader retrieves the image (7b) referencing the registry (7a), and calls the data presenter (8) which subsequently downloads the image to the EPROM simulator (9). The user (11) connects a prior art device that is capable of reading/authenticating EPROMS. This device (10) authenticates the downloaded V-PROM image. After presentation (8), the user (11) is required to enter the results of the authenticating device's inspection (12). After this, the user can select another V-PROM image for presentation (5). When the user is finished testing V-PROMS, the screen is exited via a button press (6), the diagnostic function is ended (6a) and normal game play may resume. In the event of a V-PROM failing inspection, upon exiting the diagnostic function, the game enters a lock-up state which can only be cleared by a special process, not depicted.

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Figure 3 illustrates the protocol that is used to download data to the EPROM simulator. The data is communicated via a parallel I/O cable. The data is synchronously clocked into the simulator by pulsing the strobe line. The destination address is incremented by the autofeed line. The init line is used to reset the destination address counter to zero.

It will be appreciated by persons skilled in the art that numerous variations and/or modifications may be made to the invention as shown in the specific embodiments without departing from the spirit or scope of the invention as broadly described. The present embodiments are, therefore, to be considered in all respects as illustrative and not restrictive.

CLAIMS:

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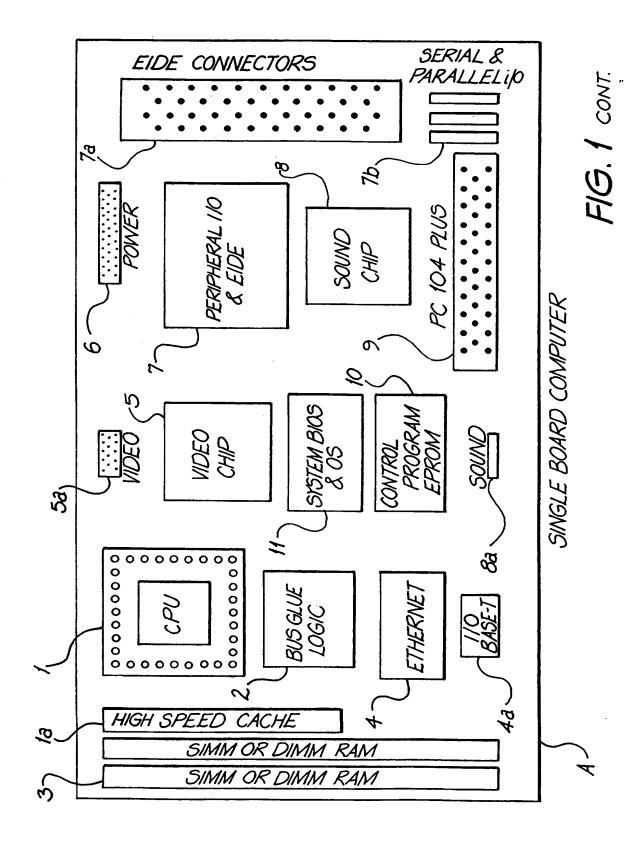
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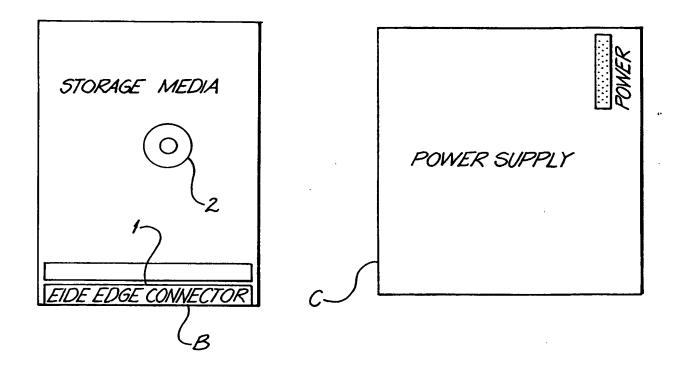
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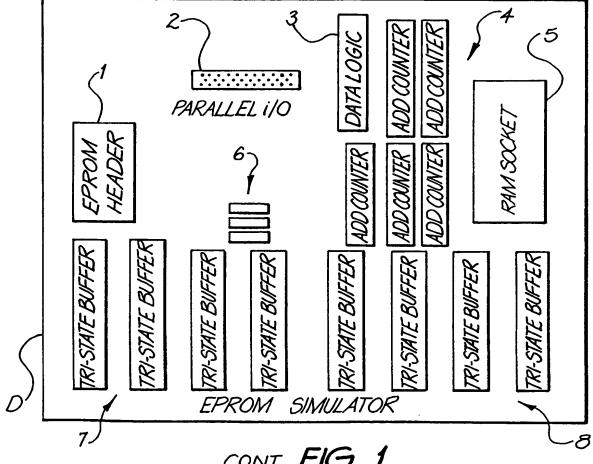
- 1. A control apparatus for emulating the physical characteristics of binary data stored in EPROM media for use with a digital processing device, comprising a CPU, operating system, dynamic memory, input/output capability and executable software, including:
 - a. a control means:
 - b. a non-volatile storage means;
 - c. at least one V-PROM resident on said non-volatile storage means;
 - d. a communications means between said V-PROM and said CPU;
- e. a presentation means;
 - f. a means for registering authorized users of said presentation means;
 - g. a reporting means for activities of said users;
 - h. a security means for protection of contents of said V-PROM;
- i. a selection means for isolating subset of said contents of said V-PROM;
 - j. a connection means between said V-PROM and at least one external EPROM-compatible device.
- 2. The control apparatus of claim 1, wherein said control means chooses among said subsets of said contents of said V-PROM for presentation to said EPROM-compatible device.
 - 3. The control apparatus of claim 1 or 2, wherein said EPROM-compatible device comprises authentication capability.
 - 4. The control apparatus of claim 3, wherein said authentication capability is designed for gaming activities.
 - 5. The control apparatusas claimed in any one of claims 1 to 4, wherein said subsets of said contents of said V-PROM comprise gaming applications.
 - 6. A method of emulating the physical characteristics of binary data stored in EPROM media for use with a digital processing device, comprising a CPU, operating system, dynamic memory, input/output capability and executable software, said method comprising the steps of:
 - a. controlling said emulation method;
 - storing said executable software and related data on a non-volatile storage means;
- c. providing at least one V-PROM resident on said non-volatile storage means;

5

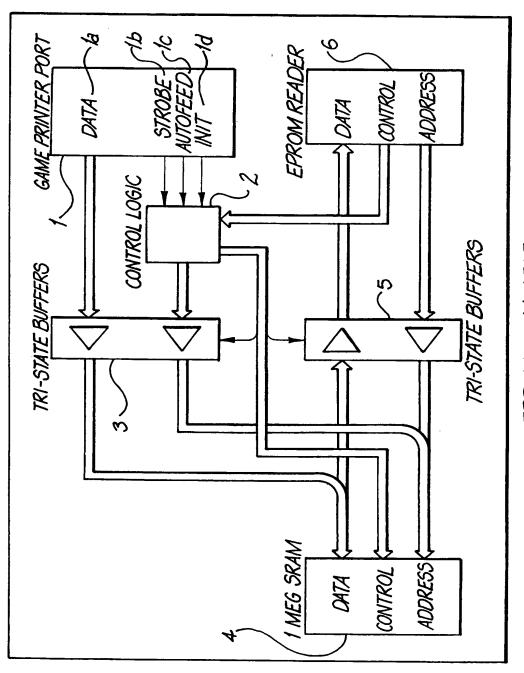
- d. providing a communications means between said V-PROM and said CPU;
- e. providing a presentation means;
- f. registering authorized users of said presentation means;
- g. reporting activities of said users;
- h. protecting contents of said V-PROM;
- i. isolating subsets of said contents of said V-PROM;
- j. providing a connection between said V-PROM and at least one external EPROM-compatible device.
- 7. The method of claim 6, wherein said software relates to the field of gaming.
 - 8. The method of claim 6 or 7, wherein said EPROM-compatible device relates to authentication activities.







CONT. FIG. 1



F16. 2

EPROM SIMULATOR

Data download protocol,

- 1. STROBE is pulled high and AUTO FEED is pulled low
- 2. <u>INIT</u> is then pulled low, (this enables the down load mode and resets the address to 0000)
- 3. Data for the current address is placed on the **DATA** lines
- 4. <u>STROBE</u> is then pulled low then high, (this will write the data into the pod ram)
- 5. <u>AUTO FEED</u> is then pulled high then low, (this will increment the counter to the next address)
- 6. Steps 3-5 are then repeated for each byte in the emulator
- 7. When data transfer is complete **INIT** must be returned high.

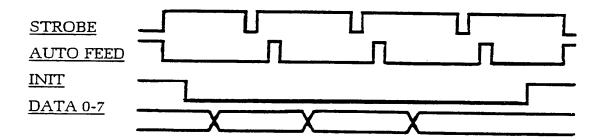
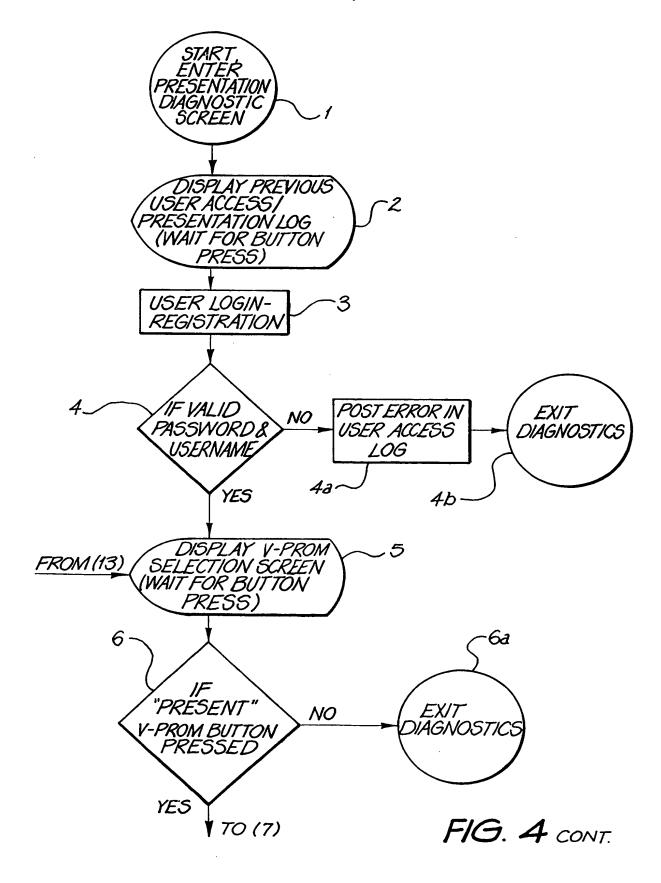
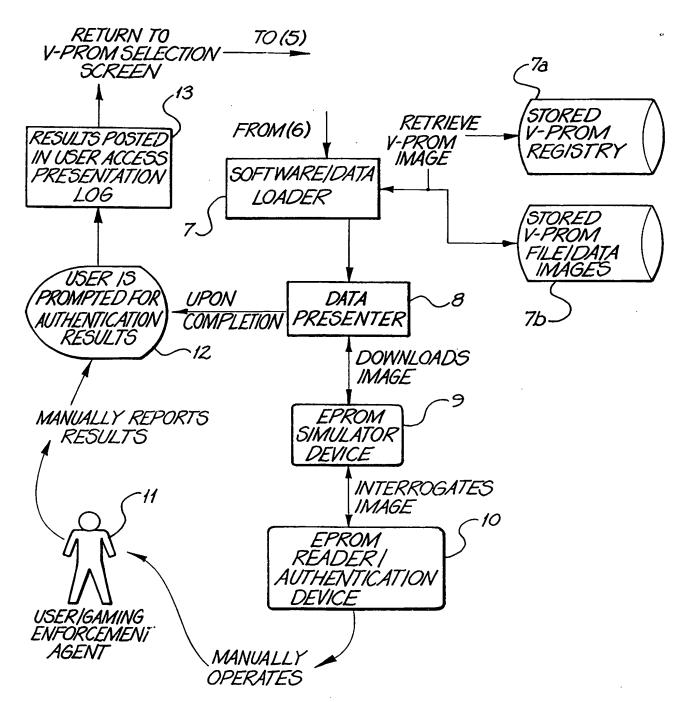


Figure 3 - Download Timing Diagram





CONT. FIG. 4

INTERNATIONAL SEARCH REPORT

International application No. **PCT/AU** 99/00511

A.	CLASSIFICATION OF SUBJECT MATTER				
Int Cl ⁶ :	G06F 017/00				
According to	International Patent Classification (IPC) or to both	n national classification and IPC			
В.	FIELDS SEARCHED				
	umentation searched (classification system followed by 6 17/00, 19/00	classification symbols)			
Documentation AU: IPC AS	n searched other than minimum documentation to the ex ABOVE	tent that such documents are included in the	he fields searched		
	a base consulted during the international search (name of tual PROM, VPROM, simulat:, emulat:, EPRO		terms used)		
C.	DOCUMENTS CONSIDERED TO BE RELEVANT	Γ			
Category*	Citation of document, with indication, where ap	propriate, of the relevant passages	Relevant to claim No.		
Α	EP 464 811, A (Matsushita Electric Industrial (Co., Ltd. 8 January 1992	1,6		
	Further documents are listed in the continuation of Box C	X See patent family and	nex		
* Speci	al categories of cited documents:	" later document published after the in	ternational filing date or		
	ment defining the general state of the art which is onsidered to be of particular relevance	priority date and not in conflict with understand the principle or theory un			
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"L" docur	ment which may throw doubts on priority claim(s) nich is cited to establish the publication date of	inventive step when the document is document of particular relevance; the			
"O" docu	ner citation or other special reason (as specified) ment referring to an oral disclosure, use,	be considered to involve an inventive combined with one or more other suc	e step when the document is ch documents, such		
"P" docu	combination being obvious to a person skilled in the art document published prior to the international filing "&" document member of the same patent family but later than the priority date claimed				
	tual completion of the international search	Date of mailing of the international searce	ch report		
19 July 1999 2 3 JUL 1999					
		Authorized officer CATHERINE REES Telephone No.: (02) 6283 2555			
Facsimile No.	: (02) 6285 3929				

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No. PCT/AU 99/00511

This Annex lists the known "A" publication level patent family members relating to the patent documents cited in the above-mentioned international search report. The Australian Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

Patent Document Cited in Search Report				Patent	Family Member	
EP	464 811	JР	4 067 471	US	5 270 877	
						END OF ANNEX

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